

FIGURE 1

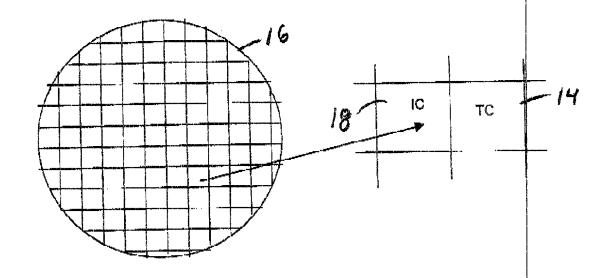
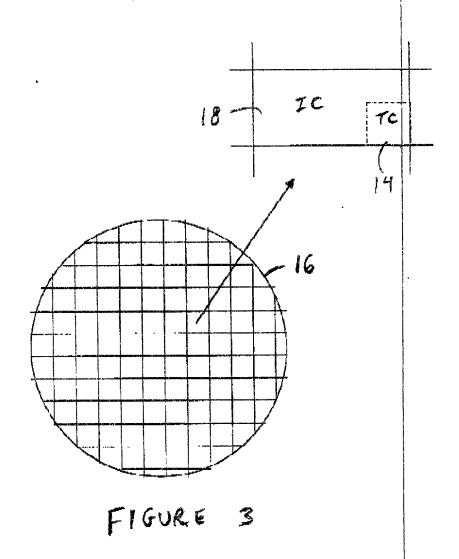


FIGURE 2



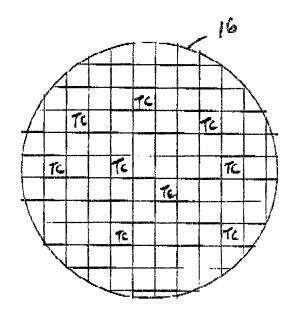


FIGURE 4

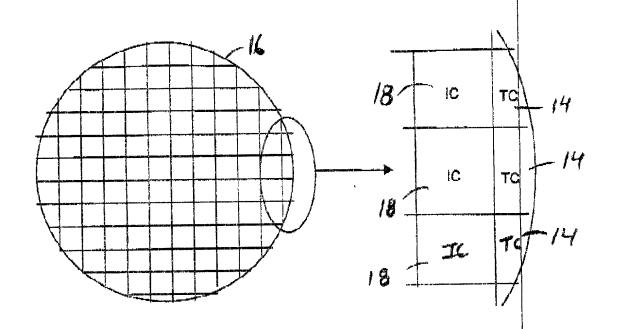


FIGURE 5

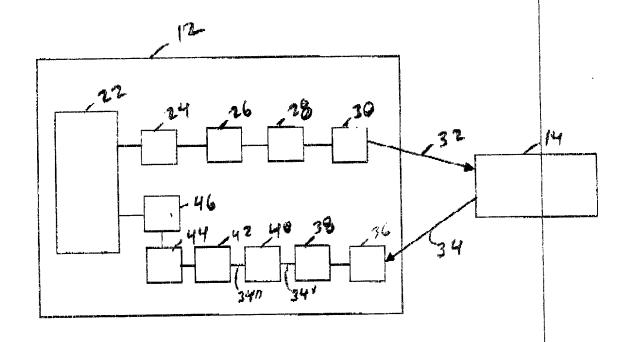


FIGURE 6

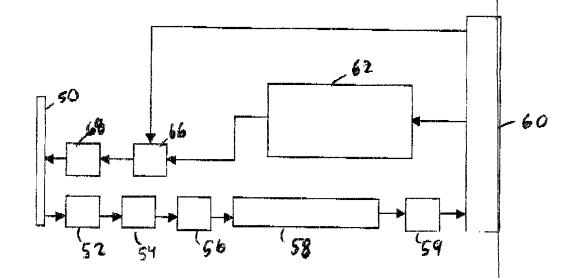


FIGURE 7.

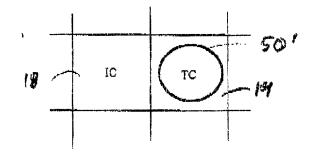
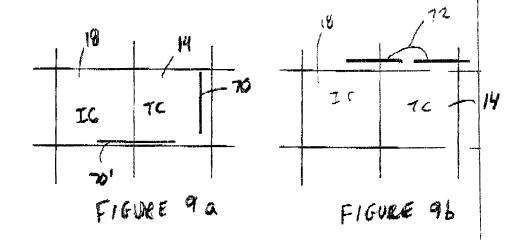
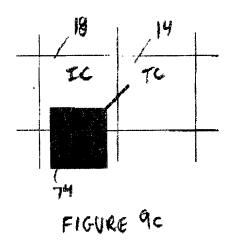
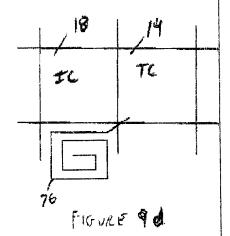


FIGURE 8







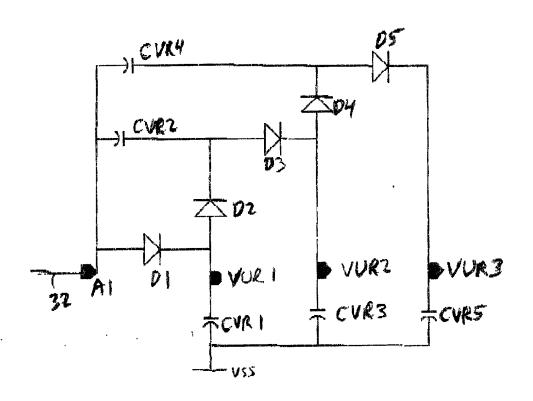


FIGURE 10.

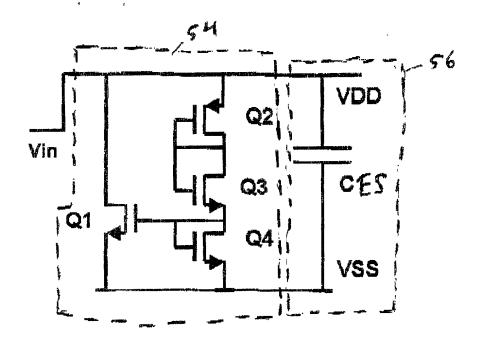


FIGURE 11

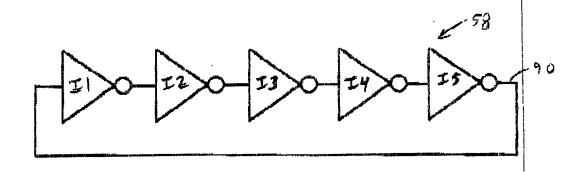


FIGURE 12

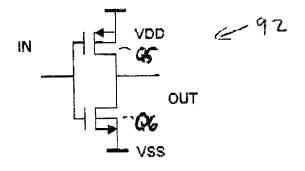


FIGURE 13

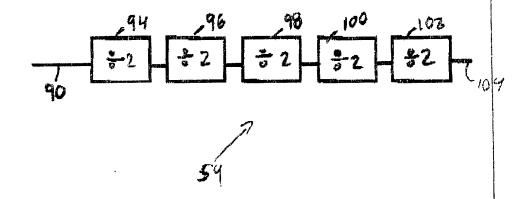


FIGURE 14.

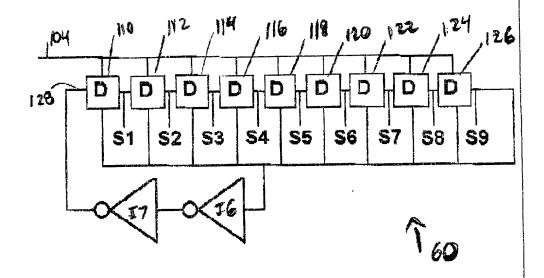


FIGURE 15

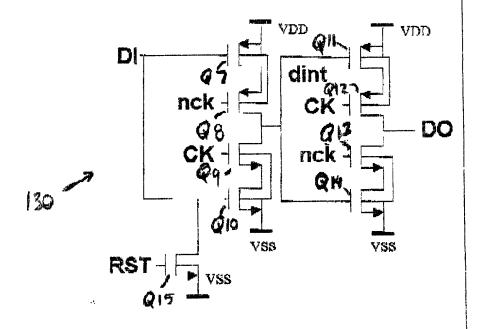
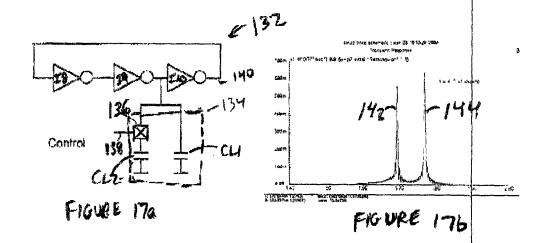
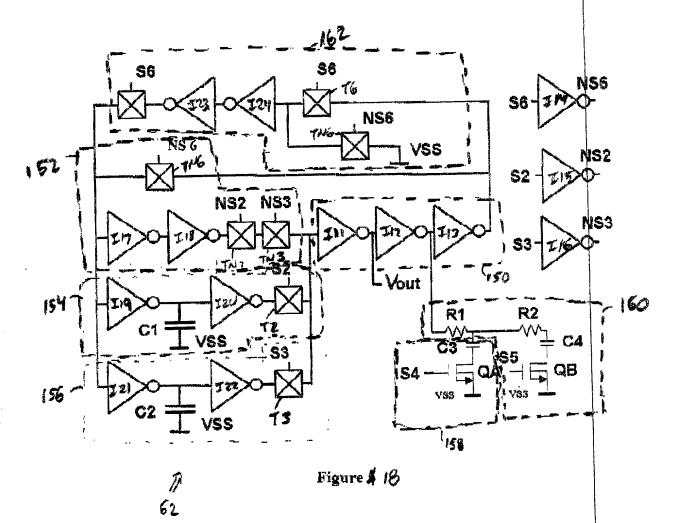


FIGURE 16





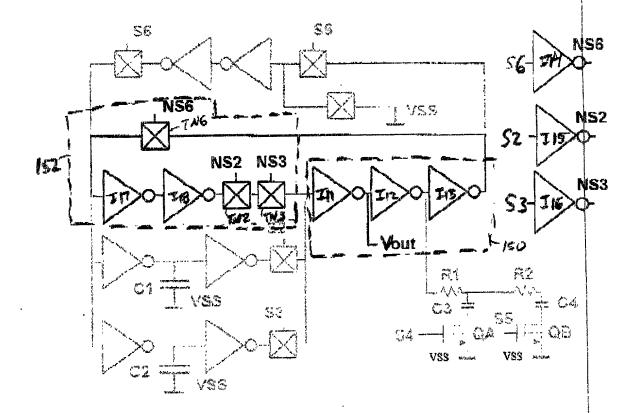
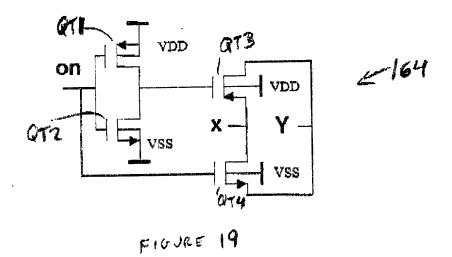


FIGURE 20



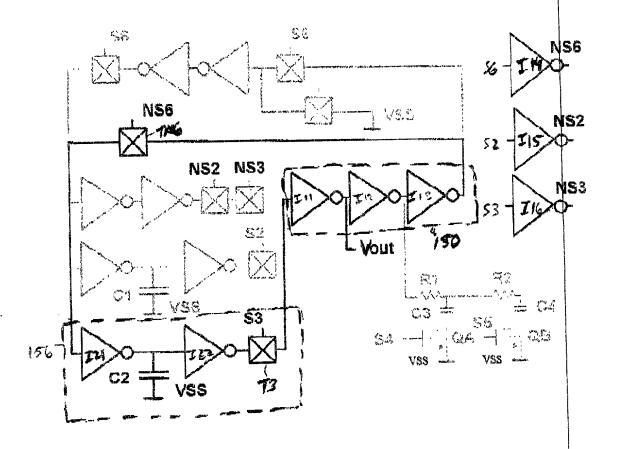


FIGURE 22

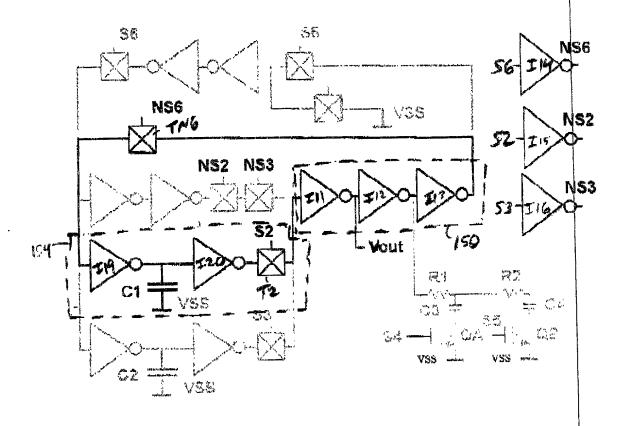


FIGURE 21

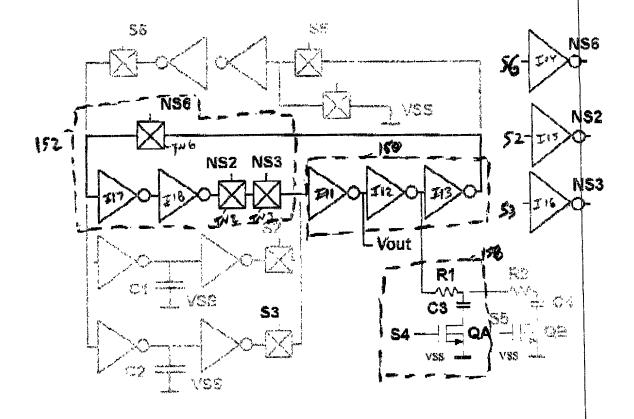


FIGURE 23

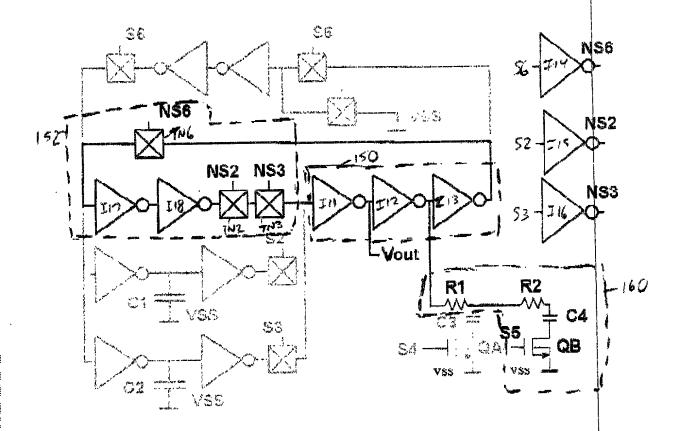


FIGURE 24

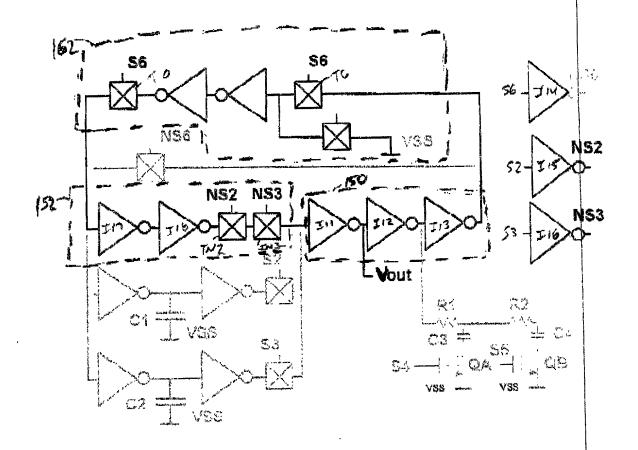


FIGURE 25

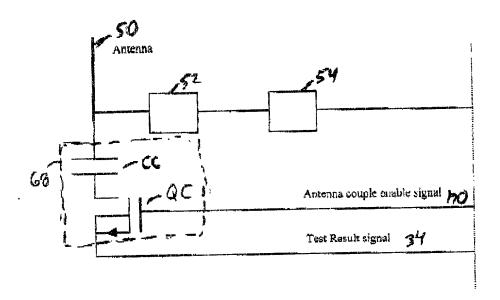


FIGURE Z6

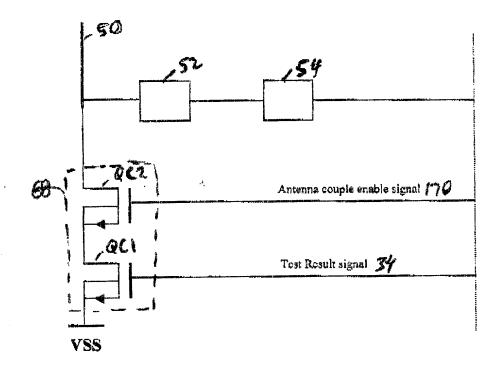


FIGURE 27

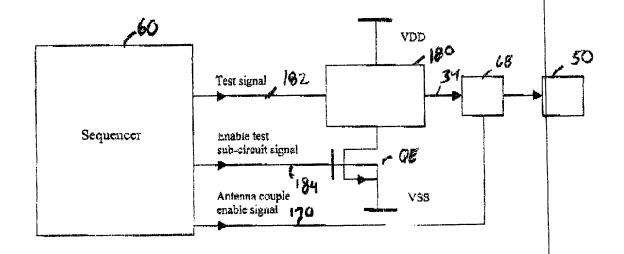


FIGURE 28

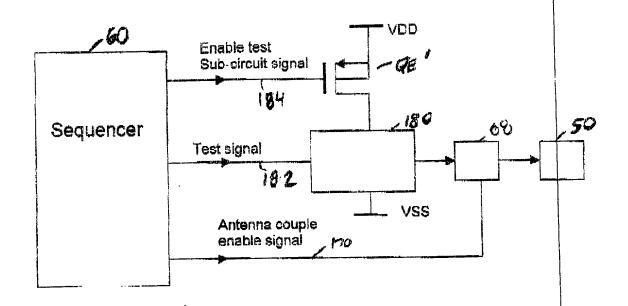


FIGURE 29

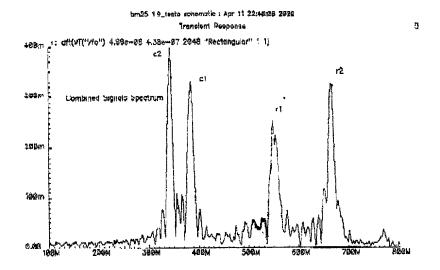


FIGURE 30a

sc882 Sinvess schematic : Mar 26 19:55:38 2296 Transient Response

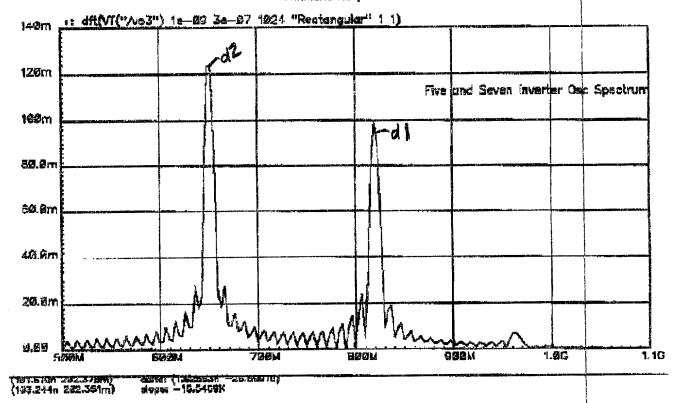


Figure 306

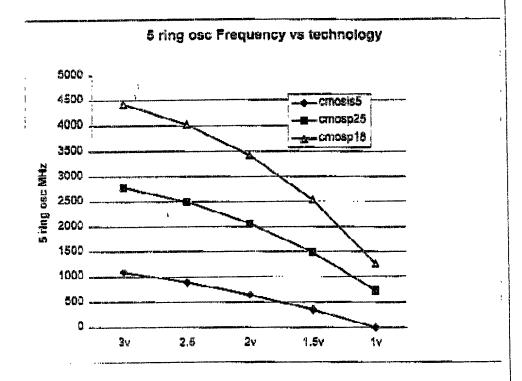


FIGURE 31